Course ID # 312L:

**Getting Started with Renesas Development Tools**

**Course Description:**
Developers creating software for embedded systems can do so more quickly and easily by applying to the fullest the features and capabilities of the latest system development tools. Thus, this DevCon 2010 laboratory explains what the current generation of tools can do and demonstrates how they can be used most effectively. Topics explored include IDEs, graphical code generation for peripheral module initialization, high-level visualization and interaction with embedded applications using Windows APIs, and run-time analyzers. Advanced debugging techniques are described and ‘tricks and tips’ for tracking down elusive bugs are presented.

*This course is part of the Development Tools track of sessions.*

**Track Description:**
Developers of software for embedded systems can make their tasks easier and gain efficiency by taking full advantage of the features and capabilities of the latest system development tools. Therefore this topic of DevCon 2010 provides details on the capabilities of the tools available for Renesas MCUs and MPUs and tips on how to use them effectively. Sessions cover subjects ranging from integrated development environments (IDEs) and graphical code generation for peripheral module initialization to high-level visualization and interaction with embedded applications using Windows APIs and run-time analyzers. Techniques are described for advanced debugging of embedded applications with state-of-the-art tools. Proven "tricks and tips" are presented for tracking down elusive bugs and problems.

**Presenter: Jim Page, Renesas Electronics America**

**Presenter Bio:**
Manager New Business Development, Renesas Electronics America
   Power Mosfets, DrMOS, and Standard Products

Work Experience
   17 Years - Hitachi, Hitachi Semiconductor, Renesas Electronics
      Business Development Manager – Power Mosfets and DrMOS
      Applications Manager - Disk Drive Spindle Motor Drivers
   8 Years - Siliconix, Inc.
      Automotive Mosfet Programs Manager
      Gate Array Design Manager
   14 Years – Fairchild Camera and Instrument, Inc.
   5 Years – Automotive Hybrid Division - Division Design Manager
   7 Years - MOS Division – Calculator and Processor Design Manager
   2 Years – Optoelectronics Division – Hand-held Calculator Design Manager
   6 years - Singer Stanford Laboratory – Future Calculator Design Researcher
   6 Years – Oregon State University, MSEE, BSEE. Feedback Control Systems
   4 Years – USAF – Non-Commissioned Officer – Korean Veteran - Japan
   4 Years –General Motors Apprenticeship
> 20 – Patents, Commendations, Medals and Papers